



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2829
Examiner: VELEZ, ROBERTO

In Re PATENT APPLICATION Of:

Applicants : Toshio TERAISHI

Appln. No. : 10 /617,817

Filed : July 14, 2003

For : ANALOG VOLTAGE OUTPUT
DRIVER LSI CHIP HAVING TEST
CIRCUIT

Attorney Ref. : 03DCOA1030

PETITION FOR
THREE-MONTH
EXTENSION OF TIME

February 13, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, Va 22313-1450

Sir:

It is respectfully requested that the period for replying to the Examiner's Action dated August 23, 2007, be extended for three months under the provisions of 37 C.F.R. 1.136(a)(1). Please charge the applicable extension fee of \$1050.00 pursuant to 37 C.F.R. 1.17(a)(3) to our Account No. 50-0945.

Should any further fees be needed, please charge it to the Account and advise us accordingly.

Respectfully submitted,

Junichi Mimura (Reg. No. 40,351)
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